FORM PTO-1449 (MODIFIED)

LIST OF PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

Applicant(s): S.R. Cebenko et al.
Case: 1-1-3-1-1
Serial No.: TBA
Filing Date: February 14, 2002
Group: TBA

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	FILING DATE IF APPROPRIATE
dlw	09/400,029	9/21/99	J.A. Schadt, "Integrated Circuit With Standard Cell Logic and Spare Gates."		

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION YES	NO
—						

OTHER DOCUMENTS

EXAMINER INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

Examiner [signature] Date Considered 03/30/04

Examiner: Initial ~~if reference~~ considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.